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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/648,689	08/25/2003	Peter W. Richards	P105-US	3780	
23494	7590 08/25/2006		EXAM	INER	
TEXAS INSTRUMENTS INCORPORATED			MENGISTU, AMARE		
P O BOX 65 DALLAS, 7	5474, M/S 3999 FX 75265		ART UNIT	PAPER NUMBER	
			2629		
			DATE MAILED: 08/25/2006		

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application	No.	Applicant(s)			
		10/648,689		RICHARDS, PETER W.			
Office Action Summary		Examiner		Art Unit			
		Amare Meng	istu	2629			
	The MAILING DATE of this communicatio	on appears on the co	over sheet with the c	orrespondence address			
Period fo	•						
WHIC - Exter after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REPORTED IN A CHEVER IS LONGER, FROM THE MAILING IN IN IT IN	NG DATE OF THIS CFR 1.136(a). In no event, on. period will apply and will ex statute, cause the applicat	COMMUNICATION however, may a reply be tim kpire SIX (6) MONTHS from the become ABANDONEC	l. ely filed the mailing date of this communication. O (35 U.S.C. § 133).			
Status							
1)⊠	Responsive to communication(s) filed on	05 June 2006.					
· · · · · · · · · · · · · · · · · · ·	This action is FINAL . 2b) This action is non-final.						
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
	closed in accordance with the practice un	nder <i>Ex parte Quay</i>	le, 1935 C.D. 11, 45	3 O.G. 213.			
Dispositi	on of Claims						
4)⊠	Claim(s) 1-26 is/are pending in the application	ation.					
· ·	4a) Of the above claim(s) <u>27-96</u> is/are withdrawn from consideration.						
5)	Claim(s) is/are allowed.						
6)⊠	☑ Claim(s) <u>1-14 and 16-26</u> is/are rejected.						
7)🖂	Claim(s) <u>15</u> is/are objected to.						
8)□	Claim(s) are subject to restriction a	and/or election req	uirement.				
Applicati	ion Papers						
9)□	The specification is objected to by the Exa	aminer.					
•	The drawing(s) filed on is/are: a)	_	objected to by the E	Examiner.			
٠	Applicant may not request that any objection t	to the drawing(s) be I	neld in abeyance. See	e 37 CFR 1.85(a).			
	Replacement drawing sheet(s) including the c	correction is required	if the drawing(s) is obj	ected to. See 37 CFR 1.121(d).			
11)	The oath or declaration is objected to by the	he Examiner. Note	the attached Office	Action or form PTO-152.			
Priority (ınder 35 U.S.C. § 119			·			
12)	Acknowledgment is made of a claim for fo	oreign priority unde	r 35 U.S.C. § 119(a)	-(d) or (f).			
a)	☐ All b)☐ Some * c)☐ None of:						
	1. Certified copies of the priority docu	ıments have been ı	received.				
	2. Certified copies of the priority docu		• •				
	3. Copies of the certified copies of the	•		ed in this National Stage			
* (application from the International B	•	* **				
	See the attached detailed Office action for	a list of the certifie	a copies not receive	u.			
Attachmen	ıt(s)		_				
	ce of References Cited (PTO-892)) Interview Summary Paper No(s)/Mail Da				
3) 🔲 Infor	ce of Draftsperson's Patent Drawing Review (PTO-94 mation Disclosure Statement(s) (PTO-1449 or PTO/5 er No(s)/Mail Date	SB/08) 5		ratent Application (PTO-152)			

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DETAILED ACTION

Election/Restrictions

1. Claims 27-96 withdrawn from further consideration pursuant to 37 CFR 1.142(b) as being drawn to a nonelected group, there being no allowable generic or linking claim. Election was made without traverse in the reply filed on June 5,2006.

2. Applicant's election without traverse of Group I (claims 1-26) in the reply filed on June 5, 2006 is acknowledged.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

4. Claims 1-14,16-26 are rejected under 35 U.S.C. 102(b) as being anticipated by.

Doherty (6,201,521 B1).

As to claims 1 and 16, **Doherty ('521)** a spatial light modulator that comprises an array of pixels, wherein the pixels of each row of the array are divided into a plurality of subgroups, for producing an image, the method comprising (col.2, lines 31-35,col.4, lines 6-22):

receiving a set of pixel data streams (fig.1 (11), fig.2 (21)), wherein the pixel data of each stream represent a set of states of a pixel of the spatial light modulator during different time intervals (col.3, lines 54-60);

streams wherein the bitplane data of each stream represent the states of a plurality of pixels during one time interval, such that the bitplane data streams representing the pixels of the same subgroup are parallel and adjacent (col.2, lines 35-40).

updating the states of the pixels using the transformed bitplane data (see, col.2, lines 40-50).

As to claim 2, **Doherty** also teaches that the bitplane data representing adjacent pixels of the spatial light modulator are parallel but not adjacent (see, fig.4 "reset group").

In regard to claim 3, **Doherty** discloses that the bitplane data streams representing the odd numbered pixels of the spatial light modulator are parallel and adjacent (see, fig.4 "reset group" 1,3,5,..).

As to claim 4, **Doherty also discloses** the bitplane data streams representing the even numbered pixels of the spatial light modulator are parallel and adjacent (fig.4 "reset group" 0.2.4..).

As to claims 5 and 17, Doherty teaches does not teach that the state is selected from

an ON state and an OFF state, and in the ON state, the pixel of the spatial light modulator represents a "bright" pixel of the image, and in the OFF state, the pixel represents a "dark" pixel of the image (However, it is inherent for **Doherty**'s pixel on the ON state to be bright and in the OFF state to be dark).

As to claims 6 and 18, **Doherty** discloses that the value of the pixel data determines the time duration of the pixel in the state (fig.4 "time").

In regard to claims 7 and 19, **Doherty** also discloses that the time intervals are determined according to a pulse-width-modulation technique (see. Col.1, lines 60- col.2, lines 2, col.2, lines 37-43).

As to claims 8 and 20, **Doherty** further discloses that the time intervals are determined according to a binary-weighted pulse-width-modulation technique (see, col.2, lines 35-40).

In regard to claims 9 and 21, **Doherty** disclose storing the transformed bitplane data streams in a frame buffer having a plurality of storage regions such that the bitplane data streams representing the pixels of the same subgroup are stored consecutively in the same region of the frame buffer (see, fig.1 (14), col.3, lines 66- col.4, lines 5).

In regard to claims 10 and 22, Doherty teaches that the storing the transformed

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bitplane data streams in a frame buffer having a plurality of storage regions such that the bitplane data streams representing the pixels of separate subgroups are stored in different regions of the frame buffer (see, fig.14), col.3, lines 66- col.4, lines 5).

As to claims 11 and 23, **Doherty** also teaches that upon receiving a writing signal, retrieving the bitplane data of the first significance from a first region of the frame buffer; and writing the pixels of the spatial light modulator with the retrieved bitplane data (col.4, lines 1-7).

In regard to claims 12 and 24 **Doherty** states the step of writing the pixels further comprises: activating the pixels using a first wordline (see, col.3, lines 66- col.4, lines 5, fig. 3 first wordline is "the first row").

As to claims 13 and 25 **Doherty** discloses retrieving the bitplane data of the second significance from a second region of the frame buffer; and writing the pixels of the spatial light modulator with the retrieved bitplane data (see, col.3, lines 66- col.4, lines 5).

As to claims 14 and 26 **Doherty** also discloses the step of writing the pixels further comprises: activating the pixels using a second wordline (fig. 3 "second wordline is the second row".

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Allowable Subject Matter

5. Claim 15 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The cited prior art Doherty (6,201,521) has failed to teach applicant's claimed invention "the pixel comprises a charge pump memory cell that further comprises: a transistor having a source, a gate, and a drain; a storage capacitor having a first plate and a second plate; and wherein the source of said transistor is connected to a bitline, the gate of said transistor is connected to a wordline, and wherein the drain of the transistor is connected to the first plate of said storage capacitor forming a storage node, and wherein the second plate of said storage capacitor is connected to a pump signal"

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Amare Mengistu whose telephone number is (571) 272-7674. The examiner can normally be reached on M-F,M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sumati Lefkowitz can be reached on (571) 272-3639. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Amare Mengistu

Primary Examiner

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AM

August 18,2006